

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,023,479 B2
APPLICATION NO. : 09/853706
DATED : April 4, 2006
INVENTOR(S) : Makoto Hiramatsu et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 5

Line 10, "generate" should read --generating--.

COLUMN 10

Line 12, "CMOSprocess" should read --CMOS process--.

COLUMN 12

Line 28, "a" should read --α--.

COLUMN 17

Line 29, "CMOSSensor" should read --CMOS sensor--.

COLUMN 18

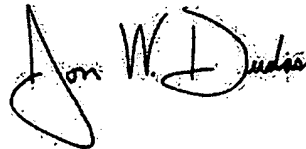
Line 40, "CMOSSensor" should read --CMOS sensor--.

COLUMN 20

Line 1, "CMOSSensor" should read --CMOS sensor--.

Signed and Sealed this

Second Day of January, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office